Docket No. 03288-1/2003-011440 FUS.065

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In repatent application of

Tsuneo Abe

Serial No.:

10/758,527

Filing Date:

January 16, 2004

Group Art Unit:

Not Yet Assigned

Examiner:

Unknown

For:

SEMICONDUCTOR DEVICE HAVING A TEST CIRCUIT FOR TESTING

AN OUTPUT CIRCUIT

Honorable Commissioner of Patents Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of these documents is discussed on pages 1, 3 and 4 of the subject application. Further, English-language Abstracts are attached to the references.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted.

Sean M. McGinn

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not considered. Include copy of this form with next communication to applicant.										